



US00D430856S

**United States Patent** [19]  
**Wilkerson**

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[45] **Date of Patent: \*\* Sep. 12, 2000**

[54] **CIRCUIT BOARD**

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[73] Assignee: **Alan W. Wilkerson**, West Bend, Wis.

[\*\*] Term: **14 Years**

[21] Appl. No.: **29/108,871**

[22] Filed: **Aug. 5, 1999**

[51] **LOC (7) Cl.** ..... **13-03**

[52] **U.S. Cl.** ..... **D13/182**

[58] **Field of Search** ..... D13/182; 361/380,  
361/384, 392, 395, 397, 399

[56] **References Cited**

**U.S. PATENT DOCUMENTS**

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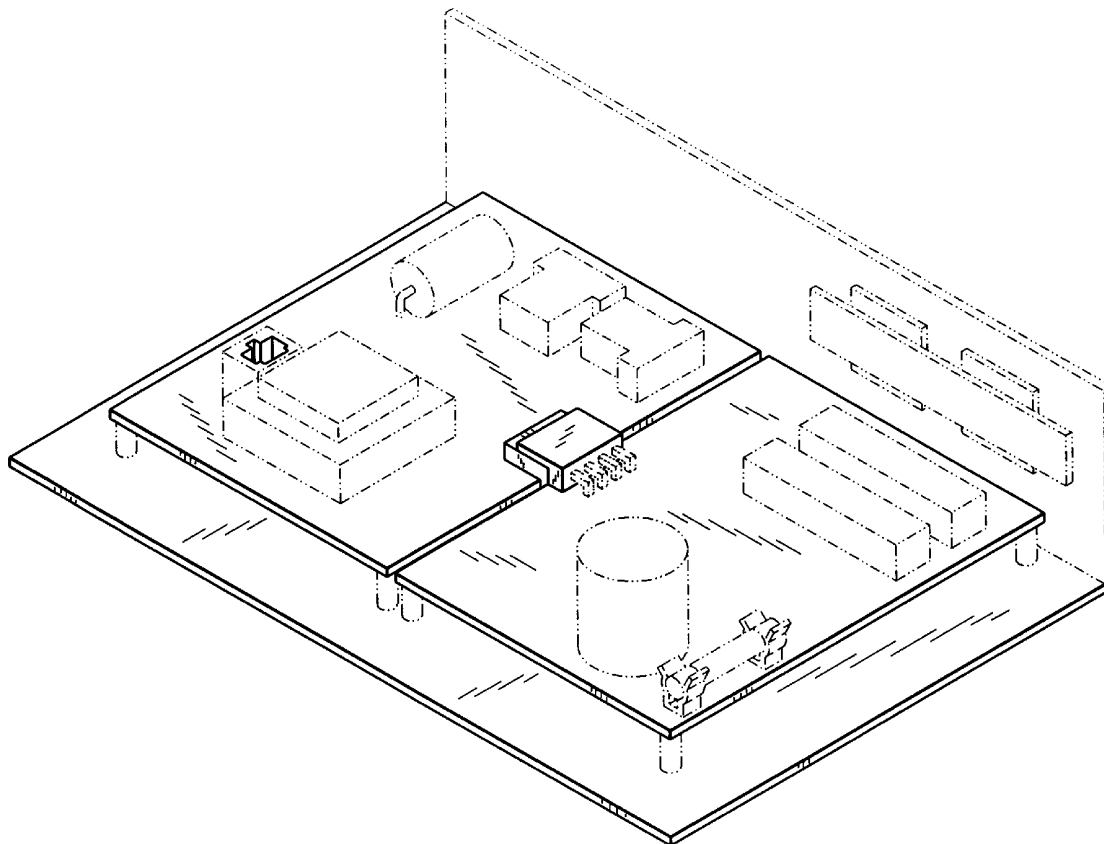
[57] **CLAIM**

The ornamental design for a circuit board, as shown and described.

**DESCRIPTION**

FIG. 1 is a perspective view of a circuit board showing my new design;  
FIG. 2 is a top plan view of the assembly of FIG. 2  
FIG. 3 is a front elevational view of the assembly of FIG. 2;  
FIG. 4 is a right side elevational view of the assembly of FIG. 2;  
FIG. 5 is a left side elevational view of the assembly of FIG. 2;  
FIG. 6 is a rear elevational view of the assembly of FIG. 2;  
and,  
FIG. 7 is a bottom plan view of the assembly of FIG. 2.  
The broken lines shown in all figures are for illustrative purposes only and form no part of the claimed design.

**1 Claim, 3 Drawing Sheets**



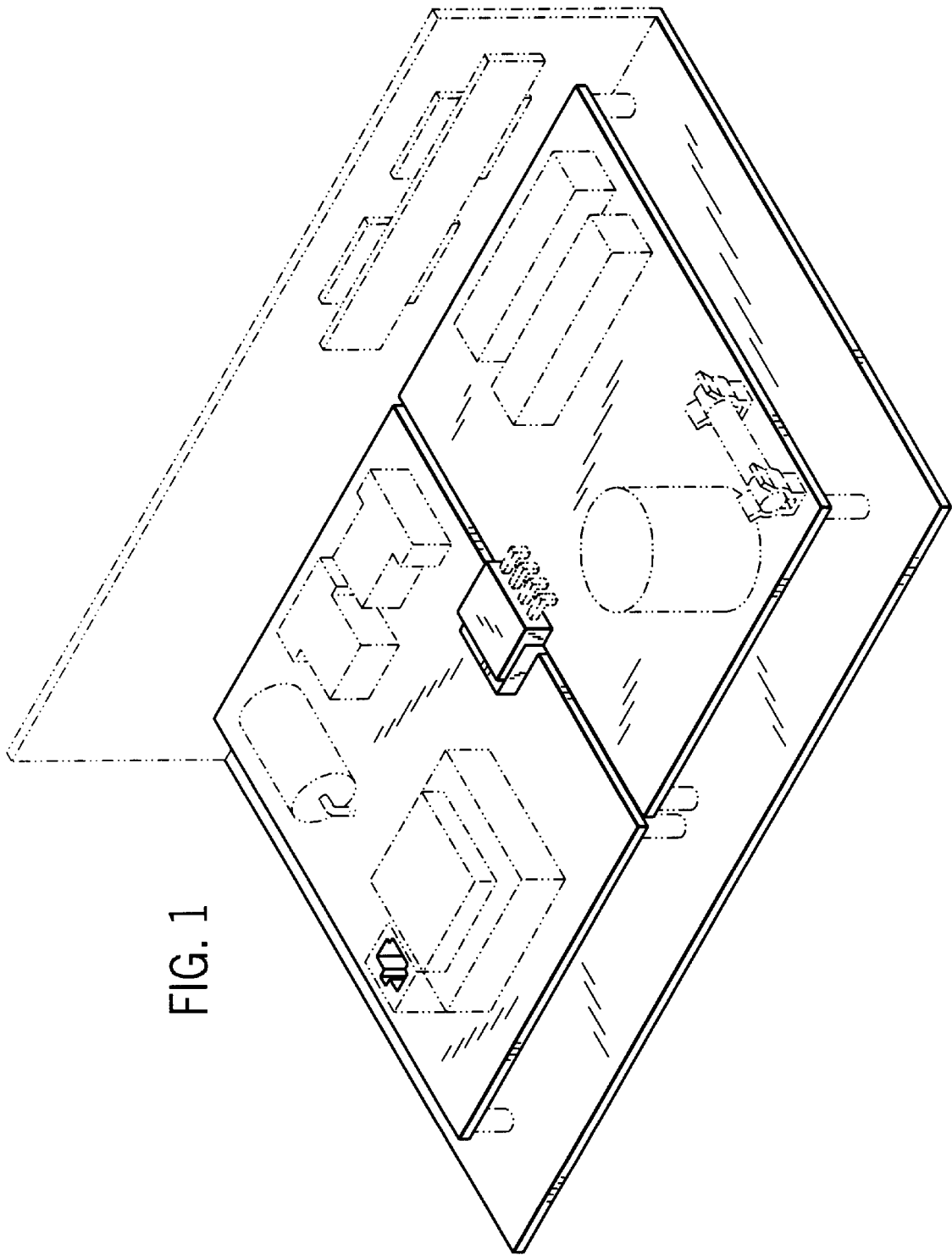


FIG. 1

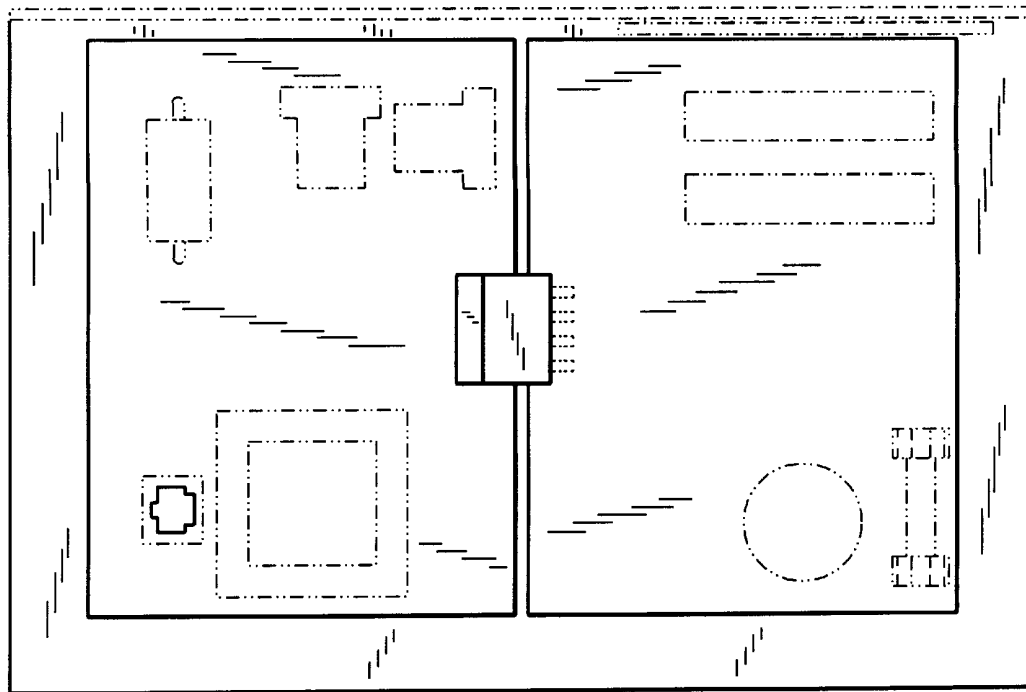
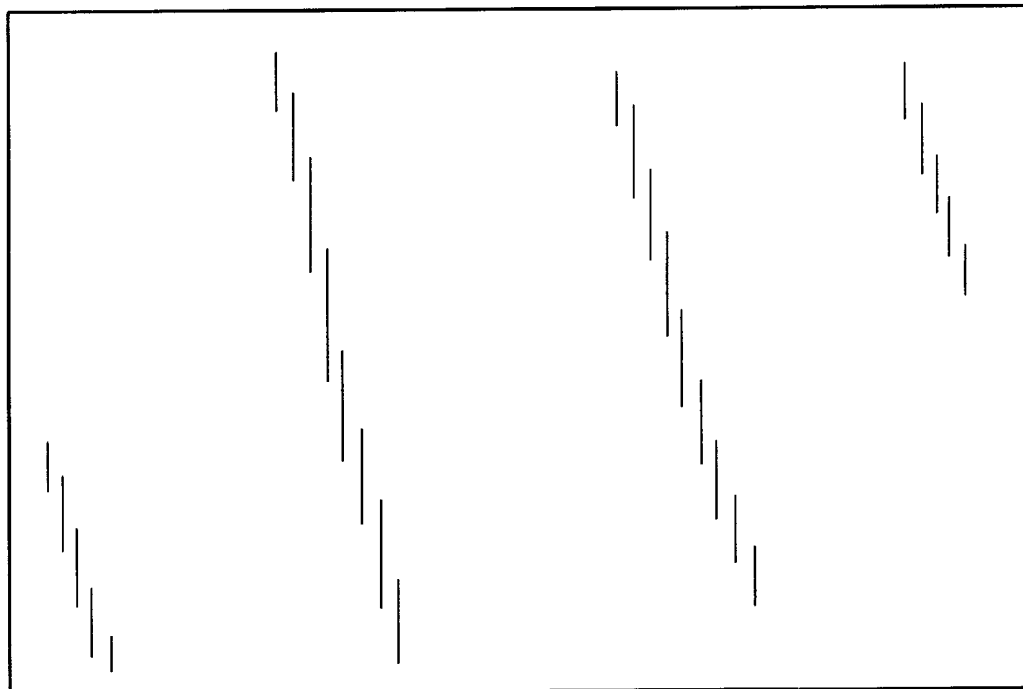


FIG. 2

FIG. 7



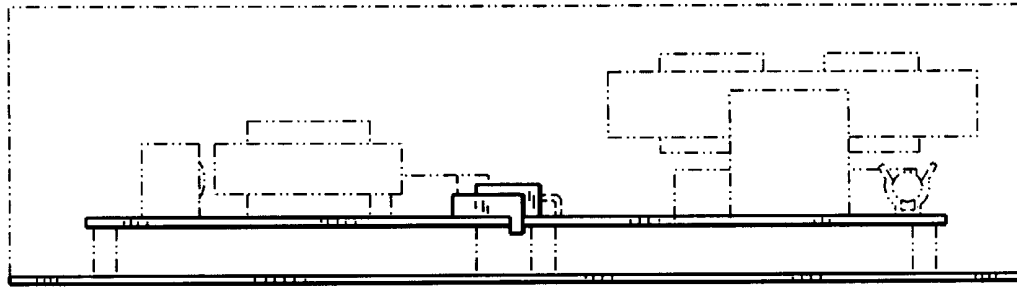


FIG. 3

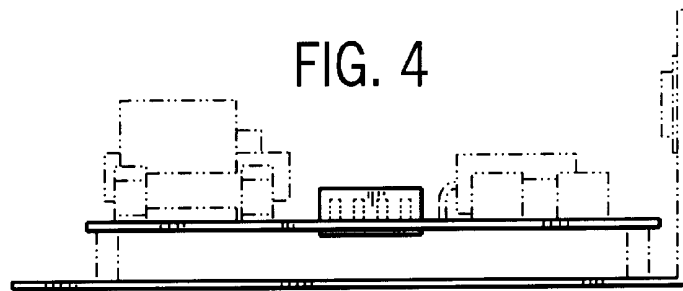


FIG. 4

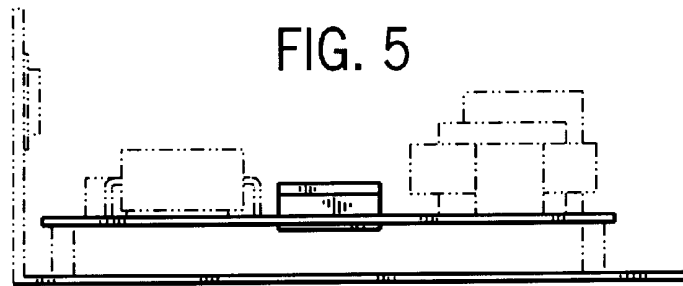


FIG. 5

FIG. 6

